

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Cheng et al.

**SERIAL NO.:** 

10/802,185

**GROUP NO.:** 

Not yet assigned

FILING DATE:

March 17, 2004

**EXAMINER:** 

Not yet assigned

TITLE:

SEMICONDUCTOR SUBSTRATE STRUCTURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

#### INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the provisions of 37 C.F.R. 1.97 and 1.98, Applicants hereby make of record the patents and publications listed on the accompanying Form PTO-1449, and other information contained herein, for consideration by the Examiner in connection with the examination of the above-identified patent application. Each of the references were previously cited in U.S. Patent Application Serial No. 10/384,160 from which this application claims priority. Accordingly, pursuant to 37 C.F.R. § 1.98(d), Applicants have not supplied copies of the references cited on the attached Form PTO-1449, but shall do so upon request.

#### REMARKS

In accordance with the provisions of 37 C.F.R. 1.97, this statement is being filed (CHECK ONE):

K)	(1)	within three (3) months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. 1.53(d), or within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. 1.491 in an international application, or before the mailing of the first Office action on the merits, or before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. 1.114; or
	(2)	after the period defined in (1) but before the mailing date of a final action or a notice of allowance under 37 C.F.R. 1.311, and
		the requisite Statement is below, OR
		the requisite fee under 37 C.F.R. 1.17(p), namely \$180.00, is included herein, or

Information Disclosure Statement Serial No. 10/802,185 Attorney Docket No. ASC-025DVC1 Page 2 after the mailing date of a final action or notice of allowance but before the payment (3) of the issue fee, AND the requisite Statement is below, AND the requisite petition fee under 37 C.F.R. 1.17(p), namely \$180.00 is included herein. In addition, Applicants wish to bring to the Examiner's attention the following co-pending patent application and office actions issued therein:

U.S. Serial No. 10/802,186, filed on 03/17/2004, by Cheng et al.

It is respectfully requested that each of the patents and publications listed on the attached Form PTO-1449, and other information contained herein, be made of record in this application.

Respectfully submitted,

Date: April 22, 2004 Reg. No. 44,381

Tel. No.: (617) 310-8327 Fax No.: (617) 248-7100

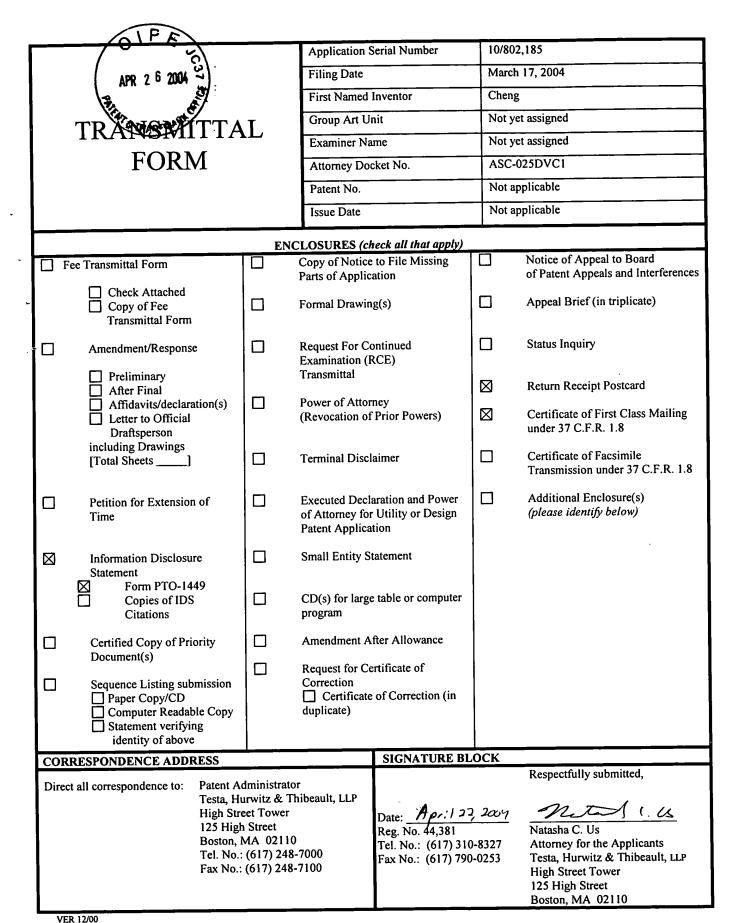
**VER 12/00** 3056069-1 Natasha C. Us

Attorney for the Applicant Testa, Hurwitz, & Thibeault, LLP

tant ( U

**High Street Tower** 125 High Street

Boston, Massachusetts 02110





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# CERTIFICATE OF FIRST CLASS MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this correspondence, and any document(s) referred to as enclosed herein, is/are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 22 day of April, 2004.

Wendy Martin

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith are: Transmittal Form (1 page); Information Disclosure Statement (2 pages); Form PTO-1449 (17 pages); and a return receipt postcard.

VER 9/00 3056923-1 FORM PTO - 1449
INFORMATION DISCLOSURE STATEMENT

APR 2 6 2004

ATTY DOCKET NO.:

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EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME		CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	Al	4,010,045	03/01/1977	Ruehrv	ein ein			
	A2	4,704,302	11/03/1987	Bruel e	t al.			
	A3	4,710,788	12/01/1987	Dämbk	es et al.			
	A4	4,987,462	01/22/1991	Kim et	al.			
	A5	4,990,979	02/05/1991	Otto				
	A6	4,997,776	03/05/1991	Haramo	et al.			
<del></del>	A7	5,013,681	05/07/1991	Godbey	et al.			
	A8	5,155,571	10/13/1992	Wang 6	t al.			
-	A9	5,166,084	11/24/1992	Pfiester				
	A10	5,177,583	01/05/1993	Endo e	al.			
	A11	5,202,284	04/13/1993	Kamins	et al.		1	
<u> </u>	A12	5,207,864	05/04/1993	Bhat et	al.			
	A13	5,208,182	05/04/1993	Naraya	n et al.			
	A14	5,212,110	05/18/1993	Pfiester	et al.			
	A15	5,221,413	06/22/1993	Brasen	et al.		1	
	A16	5,240,876 A	08/31/1993	Gaul et	al.			
	A17	5,241,197	08/31/1993	Muraka	mi et al.			
	A18	5,250,445	10/05/1993	Bean et	al.			
	A19	5,285,086	02/08/1994	Fitzger	ald			
	A20	5,291,439	03/01/1994	Kauffm	ann et al.			
	A21	5,298,452	03/29/1994	Meyers	on			
	A22	5,310,451	05/10/1994	Tejwan	i et al.			
	A23	5,316,958	05/31/1994	Meyers	on			
_	A24	5,346,848	09/13/1994	Grupen	-Shemansky et al.			
	A25	5,374,564	12/20/1994	Bruel	· · · · · ·	1		
	A26	5,399,522	03/21/1995	Ohori	<del></del>	1		
	A27	5,413,679	05/09/1995	Godbey	,			
EXAM	IINER	<u> </u>	- 1		DATE CONSID	ERED	•	,

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	A28	5,424,243	06/13/1995	Takasak	i			
	A29	5,426,069	06/20/1995	Selvaku	mar et al.			
-:	A30	5,426,316	06/20/1995	Mohami	nad			
	A31	5,442,205	08/15/1995	Brasen e	et al.	<u> </u>		
	A32	5,461,243	10/24/1995	Ek et al.				
	A33	5,461,250	10/24/1995	Burghar	tz et al.			
	A34	5,462,883	10/31/1995	Dennard	et al.			
•	A35	5,476,813	12/19/1995	Naruse				
	A36	5,479,033	12/26/1995	Baca et	al.			
	A37	5,484,664	01/16/1996	Kitahara	et al.			
	A38	5,523,243	06/04/1996	Mohami	nad			
	A39	5,523,592	06/04/1996	Nakagav	va et al.			
	A40	5,534,713	07/09/1996	Ismail et al.				
	A41	5,536,361	07/16/1996	Kondo e	t al.			
	A42	5,540,785	07/30/1996	Dennard	et al.			
	A43	5,596,527	01/21/1997	Tomiok	a et al.			
	A44	5,617,351	04/01/1997	Bertin e	al.			
	A45	5,630,905	05/20/1997	Lynch e	t al.			
	A46	5,659,187	08/19/1997	Legoues	et al.			
	A47	5,683,934	11/04/1997	Candela	ria			
· <u>-</u>	A48	5,698,869	12/16/1997	Yoshim	et al.			
	A49	5,714,777	02/03/1998	Ismail e	al.			
	A50	5,728,623	03/17/1998	Mori				
	A51	5,739,567	04/14/1998	Wong	-			
	A52	5,759,898	06/02/1998	Ek et al.				
	A53	5,777,347	07/07/1998	Bartelin	k			
	A54	5,786,612	07/28/1998	Otani et	al.			
EXAM	IINER	<del></del>			DATE CONS	SIDERED		•

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EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME		CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A55	5,786,614	07/28/1998	Chuang	g et al.			
	A56	5,792,679	08/11/1998	Nakato	· <u> </u>			
	A57	5,808,344	09/15/1998	Ismail	et al.			
	A58	5,847,419	12/08/1998	Imai et	al.			
	A59	5,863,830	01/26/1999	Bruel e	t al.			
***	A60	5,877,070	03/02/1999	Goesel	e et al.			
	A61	5,882,987	03/16/1999	Srikrish	nnan			
	A62	5,891,769	04/06/1999	Hong e	t al.			
	A63	5,906,708	05/25/1999	Robins	on et al.	j		
	A64	5,906,951	05/25/1999	Chu et	al.			*
	A65	5,912,479	06/15/1999	Mori et	al.			
	A66	5,943,560	08/24/1999	Chang	et al.		ĺ	
	A67	5,963,817	10/05/1999	Chu et	al.			
	A68	5,966,622	10/12/1999	Levine	et al.			
	A69	5,993,677	11/30/1999	Biasse	et al.			
	A70	5,998,807	12/07/1999	Lustig	et al.			
•	A71	6,013,134	01/11/2000	Chu et	al.			
	A72	6,013,563	01/11/2000	Henley	et al.			
	A73	6,020,252	02/01/2000	Aspar e	et al.			
	A74	6,033,974	03/07/2000	Henley	et al.			
	A75	6,033,995	03/07/2000	Muller				
	A76	6,058,044	05/02/2000	Sugiura	et al.			
	A77	6,059,895	05/09/2000	Chu et	al.			
	A78	6,074,919	06/13/2000	Gardne	r et al.			
_	A79	6,096,590	08/01/2000	Chan e	t al.			
	A80	6,103,559	08/15/2000	Gardne	r et al.			
	A81	6,103,597	08/15/2000	Aspar 6	et al.			
EYAN	IINER				DATE CONSI	DERED		

**FORM PTO - 1449** 

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EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME		CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
-	A82	6,103,599	08/15/2000	Henley	et al.			
-	A83	6,107,653	08/22/2000	Fitzger	ald			
	A84	6,111,267	08/29/2000	Fischer	et al.			
	A85	6,117,750	09/12/2000	Bensah	el et al.			
	A86	6,130,453	10/10/2000	Mei, et	al.			
	A87	6,133,799	10/17/2000	Favors	Jr., et al.			
•	A88	6,140,687	10/31/2000	Shimor	nura et al.		· ·	
	A89	6,143,636	11/07/2000	Forbes	et al.			
	A90	6,153,495	11/28/2000	Kub et	al.			
	A91	6,154,475	11/28/2000	Soref e	et al.			
	A92	6,160,303	12/12/2000	Fattaru	so			
	A93	6,162,688	12/19/2000	Gardne	r et al.			
	A94	6,162,705	12/19/2000	Henley	et al.			
	A95	6,184,111	02/06/2001	Henley	et al.			
	A96	6,190,998 B1	02/20/2001	Bruel e	t al.			
	A97	6,191,007	02/20/2001	Matsui	et al.			
	A98	6,191,432	02/20/2001	Sugiya	na et al.			
	A99	6,194,722	02/27/2001	Howe e	et al.			
	A100	6,204,529	03/20/2001	Lung, e	t al.			
	A101	6,207,977	03/27/2001	August	0			
	A102	6,210,988	04/03/2001	Howe e	et al.			
	A103	6,218,677	04/17/2001	Broeka	ert			
	A104	6,225,192 B1	05/01/2001	Aspar e	et al.			
	A105	6,232,138	05/15/2001	Fitzger	ald et al.			
	A106	6,235,567	05/22/2001	Huang	<del></del>			
	A107	6,242,324	06/05/2001	Kub et	al.			
	A108	6,249,022	06/19/2001	Lin, et	al.			
EXAM	IINER	*····	<u> </u>		DATE CONSIDI	ERED	-	

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EXAM. INIT.	DOCUMENT NUMBER		DATE	NAME		CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A109	6,251,751 B1	06/26/2001	Chu et a	ıl.			
	A110	6,251,755	06/26/2001	Furukav	va et al.			
	A111	6,261,929	07/17/2001	Gehrke	et al.			
	A112	6,266,278	07/24/2001	Harari,	et al.			
	A113	6,271,551	08/07/2001	Schmitz	et al.			
· -	A114	6,271,726	08/07/2001	Fransis	et al.			
	A115	6,290,804 B1	09/18/2001	Henley	et al.			02/20/1998
	A116	6,291,321	09/18/2001	Fitzgera	ıld			03/09/1999
	A117	6,303,468 B1	10/16/2001	Aspar e	t al.		:	10/16/2001
	A118	6,313,016	11/06/2001	Kibbel	et al.			12/22/1999
	A119	6,316,301	11/13/2001	Kant				03/08/200
	A120	6,323,108	. 11/27/2001	Kub et	al.			07/27/1999
	A121	6,326,667 B1	12/04/2001	Sugiyar	na et al.			09/08/2000
	A122	6,329,063	12/11/2001	Lo et al				12/11/1998
	A123	6,335,546	01/01/2002	Tsuda e	t al.			07/30/1999
··	A124	6,339,232	01/15/2002	Takagi			1	09/20/1999
	A125	6,344,417 B1	02/05/2002	Usenko				08/08/2000
	A126	6,346,459 B1	02/12/2002	Usenko	et al.			02/02/2000
	A127	6,350,993	02/26/2002	Chu et a	al. ,			03/12/1999
	A128	6,352,909 B1	03/05/2002	Usenko				05/26/2000
	A129	6,355,493 B1	03/12/2002	Usenko	-			06/30/2000
	A130	6,368,733	04/09/2002	Nishina	ga			08/05/1999
	A131	6,368,938 B1	04/09/2002	Usenko			:	06/07/2000
	A132	6,369,438 B1	04/09/2002	Sugiyar	na et al.			12/22/2000
	A133	6,372,356	04/16/2002	Thornto	n et al.			04/28/2000
	A134	6,372,593 B1	04/16/2002	Hattori	et al.			07/19/2000
	A135	6,372,609 B1	04/16/2002	Aga et a	al.			10/08/1999

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U.S. PATENT D	OCUMENTS
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EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A136	6,387,829 B1	05/14/2002	Usenko et al.			04/06/2000
	A137	6,391,740 B1	05/21/2002	Cheung et al.		İ	04/28/1999
	A138	6,399,970	06/04/2002	Kubo et al.			09/19/1997
-	A139	6,403,975	06/11/2002	Brunner et al.			04/08/1997
	A140	6,407,406	06/18/2002	Tezuka			06/29/1999
	A141	6,410,371 B1	06/25/2002	Yu et al.			02/26/2001
	A142	6,425,951	07/30/2002	Chu et al.			08/06/1999
	A143	6,429,061	08/06/2002	Rim			07/26/2000
	A144	6,445,016 B1	09/03/2002	An et al.			02/28/2001
•	A145	6,448,152 B1	09/10/2002	Henley et al.		-	07/16/2001
	A146	6,455,397 B1	09/24/2002	Belford			11/09/2000
	A147	6,458,672 B1	10/01/2002	Henley et al.			11/02/2000
	A148	6,475,072 B1	11/05/2002	Canaperi et al.			09/29/2000
	A149	6,514,836 B2	02/04/2003	Belford		1	06/04/2001
	A150	6,515,335 B1	02/04/2003	Christiansen et al.			01/04/2002
	A151	6,521,041	02/18/2003	Wu et al.			04/09/1999
	A152	6,524,935 B1	02/25/2003	Canaperi et al.			09/29/2000
	A153	6,534,381 B2	03/18/2003	Cheung et al.		1	01/04/2000
	A154	6,555,839	04/29/2003	Fitzgerald et al.		Ì	05/16/2001
	A155	6,573,126	06/03/2003	Cheng et al.			08/10/2001
-	A156	6,583,015	06/24/2003	Fitzgerald et al.			08/06/2001
	A157	6,583,437 B2	06/24/2003	Mizuno et al.			03/19/2001
	A158	6,593,191	07/15/2003	Fitzgerald			05/16/2001
	A159	6,593,625 B2	07/15/2003	Christiansen et al.			04/03/2002
	A160	6,596,610 B1	07/22/2003	Kuwabara et al.			11/27/2000
	A161	6,602,613	08/05/2003	Rim			01/17/2001
	A162	6,603,156	08/05/2003	Fitzgerald			03/31/2001

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EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME		CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A163	6,607,948 B1	08/19/2003	Sugiya	ma et al.			08/24/2001
	A164	6,624,047 B1	09/23/2003	Sakagu	chi et al.			02/01/2000
	A165	6,624,478 B2	09/23/2003	Anders	on et al.			01/30/2002
	A166	6,632,724 B2	10/14/2003	Henley	et al.			01/13/2000
	A167	6,635,909 B2	10/21/2003	Clark e	t al.			03/19/2002
	A168	6,645,831 B1	11/11/2003	Shahee	n et al.			05/07/2002
•••	A169	6,649,492 B2	11/18/2003	Chu et	al.			02/11/2002
	A170	6,656,271 B2	12/02/2003	Yoncha	ura et al.			12/03/1999
	A171	6,664,169 B1	12/16/2003	Iwasaki	et al.			06/05/2000
	A172	6,677,183 B2	01/13/2004	Sakagu	chi et al.			01/31/2002
	A173	6,680,240 B1	01/20/2004	Maszar	a			06/25/2002
	A174	6,680,260 B2	01/20/2004	Akiyan	na et al.			09/17/2002
	A175	6,690,043 B1	02/10/2004	Usuda	et al.		1	11/22/2000
	A176	6,706,614 B1	03/16/2004	An et a	1.			05/15/2002
	A177	6,706,618 B2	03/16/2004	Takisav	va et al.			07/29/2002
· <del>-</del>	A178	6,707,106 B1	03/16/2004	Wrister	s et al.			10/18/2002
	A179	6,709,903 B2	03/23/2004	Christia	ansen et al.			04/30/2003
	A180	6,709,909 B2	03/23/2004	Mizuno	et al.			05/19/2003
	A181	6,713,326 B2	03/30/2004	Cheng	et al.			03/04/2003
,	A182	2001/0003364	06/14/2001	Sugawa	ara et al.			12/08/2000
	A183	2001/0007789 A1	07/12/2001	Aspar e	et al.			02/26/2001
	A184	2002/0043660	04/18/2002	Yamaz	aki et al.			06/25/2001
	A185	2002/052084	05/02/2002	Fitzger	ald			05/16/2001
	A186	2002/096717	07/25/2002	Chu et	al.			01/25/2001
	A187	2002/0100942	08/01/2002	Fitzger	ald et al.			08/01/2002
	A188	2002/0123167	09/05/2002	Fitzger	ald			07/16/2001
	A189	2002/0123183	09/05/2002	Fitzger	ald			07/16/2001
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	A190	2002/0123197	09/05/2002	Fitzger	ald et al.	1		06/19/2001
	A191	2002/0125471	09/12/2002	Fitzger	ald et al.			12/04/2001
	A192	2002/0125497	09/12/2002	Fitzger	ald			07/16/2001
	A193	2002/0168864	11/14/2002	Cheng	et al.			04/04/2002
-	A194	2003/0003679	01/02/2003	Doyle o	et al.		1	06/29/2001
	A195	2003/0013305 A1	01/16/2003	Sugii e	t al.			6/17/2002
	A196	2003/0013323	01/16/2003	Hammo	ond et al.			6/14/2002
	A197	2003/0025131	02/06/2003	Lee et a	al.			08/02/2002
<del></del>	A198	2003/0034529	02/20/2003	Fitzger	ald et al.			10/08/2002
	A199	2003/0057439	03/27/2003	Fitzger	ald			08/09/2002
	A200	2003/0077867	04/04/2003	Fitzger	ald			07/16/2001
	A201	2003/0102498	06/05/2003	Braithv	vaite et al.			09/24/2002
	A202	2003/0119280 A1	06/26/2003	Lee et a	al.		İ	12/02/2002
	A203	2003/0127646 A1	07/10/2003	Christia	ansen et al.			12/18/2002
	A204	2003/0139000 A1	07/24/2003	Bedell	et al.			01/23/2002
	A205	2003/0157787 A1	08/21/2003	Murthy	et al.			02/21/2002
	A206	2003/0160300 A1	08/28/2003	Takena	ka et al.			02/24/2003
	A207	2003/0168654 A1	09/11/2003	Cheng	et al.			03/07/2003
	A208	2003/0178681 A1	09/25/2003	Clark e	t al.			04/02/2003
	A209	2003/0189229 A1	10/09/2003	Mouli	•			04/05/2002
	A210	2003/0199126 A1	10/23/2003	Chu et	al.			04/23/2002
	A211	2003/0201458 A1	10/30/2003	Clark e	t al.			05/16/2003
	A212	2003/0203600 A1	10/30/2003	Chu et	al.			06/05/2003
	A213	2003/0207127 A1	11/06/2003	Murthy	et al.			05/30/2003
	A214	2003/0218189 A1	11/27/2003	Christia	ansen et al.			11/19/2002
	A215	2003/0219957 A1	11/27/2003	Kuwab	ara et al.			05/29/2003
	A216	2003/0227036 A1	12/11/2003	Sugiya	ma et al.			02/21/2003
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SHEET 9 OF 17 ATTY DOCKET NO.: **FORM PTO - 1449** ASC-025DVC1 APPLICANTS: Cheng et al. INFORMATION DISCLOSURE STATEMENT SERIAL NO.: 10/802,185 FILING DATE: March 17, 2004 GROUP: Not yet assigned U.S. PATENT DOCUMENTS DOCUMENT DATE NAME CLASS SUB FILING DATE IF EXAM. CLASS APPROPRIATE INIT. NUMBER A217 2003/0227057 12/01/2003 Lochtefeld et al. 10/04/2002 A218 01/30/2003 2003/0230778 A1 12/18/2003 Park et al. A219 2003/0232467 A1 12/18/2003 Anderson et al. 05/29/2003 A220 2004/0005740 01/01/2004 Lochtefeld et al. 06/06/2003 A221 2004/0007724 A1 01/15/2004 Murthy et al. 07/12/2002 A222 2004/0009649 A1 01/15/2004 Kub et al. 05/20/2003 A223 2004/0012037 A1 01/22/2004 Venkatesan et al. 07/18/2002 A224 01/22/2004 Bedell et al. 07/16/2002 2004/0012075 A1 A225 07/18/2002 2004/0014304 A1 01/22/2004 Bhattacharyya A226 01/29/2004 07/24/2002 2004/0018699 A1 Boyd et al. A227 2004/0031979 02/19/2004 Lochtefeld et al. 06/06/2003 A228 2004/0031990 A1 02/19/2004 Jin et al. 08/16/2002 A229 03/04/2004 Okihara 03/21/2003 2004/0041174 A1 A230 2004/0041210 A1 03/04/2004 Mouli 09/02/2003 A231 09/04/2003 2004/0048091 A1 03/11/2004 Sato et al. A232 2004/0048454 A1 03/11/2004 Sakaguchi 09/04/2003 A233 2004/0051140 A1 03/18/2004 Bhattacharyya 09/12/2002 A234 2004/0053477 A1 03/18/2004 Ghyselen et al. 07/09/2003 FOREIGN PATENT DOCUMENTS EXAM. DOCUMENT DATE **COUNTRY CLASS** SUB **FILING ABSTRACT ENGLISH** CODE **CLASS** DATE **ONLY** LANG (Y/N) INIT. NUMBER 07/23/1992 DE No **B1** 41 01 167 No **B2** 0 514 018 11/19/1992 EP No Yes Yes 0 587 520 03/16/1994 EP No **B3** EP Yes **B4** 0 683 522 11/22/1995 No 0 828 296 03/11/1998 EP No Yes **B5 B6** 0 829 908 03/18/1998 EP No Yes 04/29/1998 EP **B7** 0 838 858 No No

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ASC-025DVC1

APPLICANTS:

Cheng et al.

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	B8	1 020 900	07/19/2000	EP					No	Yes	
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	B10	2 342 777	04/19/2000	GB					Yes	Yes	
	B11	4-307974	10/30/1992	JP					No	No	
	B12	5-166724	07/03/1993	JP					No	Abstract Only	
	B13	6-177046	06/24/1994	JP			:		No	Abstract Only	
	B14	7-106446	04/21/1995	JP					No	No	
	B15	7-240372	09/12/1995	JР	_				No	Abstract Only	
	B16	10-270685	10/09/1998	JP					No	Yes	
	B17	11-233744	08/27/1999	JP	·				No	No	
	B18	2000-021783	01/21/2000	JP					No	Yes	
	B19	2000-31491	01/28/2000	JP					No	No	
	B20	2001319935	05/11/2000	JP					Yes	Yes	
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	B23	2002-289533	10/04/2002	JP					No	Yes	
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	B25	WO 99/53539	10/21/1999	PCT					No	Yes	
	B26	WO 00/48239	08/17/2000	PCT					No	Yes	
	B27	WO 01/54202	07/26/2001	PCT					No	Yes	
	B28	WO 01/99169A2	12/27/2001	PCT					No	Yes	
_	B29	WO 02/15244 A2	02/21/2002	PCT					No	Yes	
	B30	WO 02/27783 A1	04/04/2002	РСТ					No	Yes	
	B31	WO 02/071495A1	09/12/2002	PCT					No	Yes	
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	B32	WO 02/082514 A1	10/17/2002	РСТ					No	Yes
	B33	WO 00/54338	09/14/2000	wo					No	Yes
	B34	WO 01/022482	03/29/2001	wo					No	Yes
	B35	WO 01/93338	12/06/2001	wo					No	Yes
	B36	WO 02/13262	02/14/2002	wo					No	Yes
	B37	WO 02/47168	06/13/2002	wo					No	Yes
	B38	WO 02/071488	09/12/2002	wo					No	Yes
	B39	WO 02/071491	09/12/2002	wo					No	Yes
	B40	WO 04/006311 A2	01/15/2004	wo				07/09/2003		YES
	B41	WO 04/006326 A1	01/15/2004	wo				07/09/2003		YES
	B42	WO 04/006327 A2	01/15/2004	wo				07/09/2003		YES
	B43	WO 04/019403 A2	03/04/2004	wo				08/26/2003		YES
	B44	WO 04/019404 A2	03/04/2004	wo				08/26/2003		YES
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EXAM. INIT.										
	CI	"2 Bit/Cell EEPROM Cell Using Band to Band Tunneling for Data Read-Out," IBM Technical Disclosure Bulletin, Vol. 35, No. 4B (September 1992) pp. 136-140.								osure Bulletin,
	C2	Armstrong et al., "Design of Si/SiGe Heterojunction Complementary Metal-Oxide-Semiconductor Transistors," <u>IEDM Technical Digest</u> (1995) pp. 761-764.								ansistors,"
	C3	Armstrong, "Technology for SiGe Heterostructure-Based CMOS Devices", Ph.D Thesis, Massachusetts Institute of Technology (1999) pp. 1-154.								etts Institute of
	C4	Augusto et al., "Pr MOSFETs withou								ry MOD-
	C5	Barradas et al., "R channels for HM0	•	-					in, high Ge cont	ent SiGe
EXAMI	INER		<del> </del>		D	DATE CONSIDERED				

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FORM	PTO –	1449	A <sup>2</sup>	TTY DOCKET NO.:	ASC-025DVC1				
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			GI	ROUP:	Not yet assigned				
		ETC.							
EXAM. INIT.	OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)								
	C6 Borenstein et al., "A New Ultra-Hard Etch-Stop Layer for High Precision Micromachining," Proceedings  1999 12 <sup>th</sup> IEEE International Conference on Micro Electro Mechanical Systems (MEMs) (January 17-21 205-210.								
	C7	Bouillon et al., "Search for the optimal c <u>IEEE</u> (1996) pp. 21.2.1-21.2.4.	hanr	nel architecture for 0.18/0	.12 μm bulk CMOS Experimental study,"				
	C8	Bruel et al., "®SMART CUT: A Promis <u>SOI Conference</u> (October 1995) pp. 178	_		ology," Proceedings 1995 IEEE International				
	C9	Bruel, "Silicon on Insulator Material Tec 1202.	chno	logy," Electronic Letters,	Vol. 13, No. 14 (July 6, 1995) pp. 1201-				
	C10	Bufler et al., "Hole transport in strained 84, No. 10 (November 15, 1998) pp. 55			ubstrates," Journal of Applied Physics, Vol.				
	C11	Burghartz et al., "Microwave Inductors and Capacitors in Standard Multilevel Interconnect Silicon Technology", IEEE Transactions on Microwave Theory and Techniques, Vol. 44, No. 1 (January 1996) pp. 100-104.							
_	C12	Canaperi et al., "Preparation of a relaxed Si-Ge layer on an insulator in fabricating high-speed semiconductor devices with strained epitaxial films," International Business Machines Corporation, USA (2002) (abstract).							
	C13	Carlin et al., "High Efficiency GaAs-on-Si Solar Cells with High Voc Using Graded GeSi Buffers," IEEE (2000) pp. 1006-1011							
	C14	Chang et al., "Selective Etching of SiGe/Si Heterostructures," <u>Journal of the Electrochemical Society</u> , No. 1 (January 1991) pp. 202-204.							
	C15	Cheng et al., "Electron Mobility Enhanc Substrates," IEEE Electron Device Lett			ETs Fabricated on SiGe-on-Insulator (SGOI) pp. 321-323.				
	C16	Cheng et al., "Relaxed Silicon-Germania Materials, Vol. 30, No. 12 (2001) pp. L			ayer Transfer," Journal of Electronic				
	C17	C17 Cullis et al, "Growth ripples upon strained SiGe epitaxial layers on Si and misfit dislocation interactions," <u>Journal of Vacuum Science and Technology A</u> , Vol. 12, No. 4 (July/August 1994) pp. 1924-1931.							
	C18	Currie et al., "Carrier mobilities and process stability of strained Si n- and p-MOSFETs on SiGe virtual substrates,"  J. Vac. Sci. Technol. B., Vol. 19, No. 6 (Nov/Dec 2001) pp. 2268-2279.							
	C19	Currie et al., "Controlling Threading Dislocation in Ge on Si Using Graded SiGe Layers and Chemical-Mechanical Polishing," vol. 72 No. 14 (April 6, 1998) pp. 1718-1720.							
	C20 Eaglesham et al., "Dislocation-Free Stranski-Krastanow Growth of Ge on Si(100)," Physical Review Letters, V 64, No. 16 (April 16, 1990) pp. 1943-1946.								
	C21	C21 Feijoo et al., "Epitaxial Si-Ge Etch Stop Layers with Ethylene Diamine Pyrocatechol for Bonded and Etchback Silicon-on-Insulator," <u>Journal of Electronic Materials</u> , Vol. 23, No. 6 (June 1994) pp. 493-496.							
	C22 Fischetti et al., "Band structure, deformation potentials, and carrier mobility in strained Si, Ge, and SiGe alloys," <u>J. Appl. Phys.</u> , Vol. 80, No. 4 (August 15, 1996) pp. 2234-2252.								
EXAMI	NER			DATE CONSIDEREI	)				

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FORM	PTO -	1449	A'	TTY DOCKET NO.:	ASC-025DVC1				
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EXAM. INIT.	ОТНЕ	ER DOCUMENTS: (Including Author,	Γitle	, Date, Relevant Pages,	Place of Publication)				
	C23	Fischetti, "Long-range Coulomb interactions in small Si devices. Part II. Effective electron mobility in thin-oxide structures," <u>Journal of Applied Physics</u> , Vol. 89, No. 2 (January 15, 2001) pp. 1232-1250.							
· · · · ·	C24	Fitzgerald et al., "Dislocation dynamics in relaxed graded composition semiconductors," Materials Science and Engineering B67 (1999) pp. 53-61.							
	C25	C25 Fitzgerald et al., "Relaxed Ge <sub>x</sub> Si <sub>1-x</sub> structures for III-V integration with Si and high mobility two-dimensional electron gases in Si," AT&T Bell Laboratories, Murray Hill, NJ 07974 (1992) American Vacuum Society, pp. 1807-1819.							
	C26	C26 Fitzgerald et al., "Totally Relaxed Ge <sub>x</sub> Si <sub>1-x</sub> Layers with Low Threading Dislocation Densities Grown on Si Substrates," Applied Physics Letters, Vol. 59, No. 7 (August 12, 1991) pp. 811-813.							
	C27	Garone et al., "Silicon vapor phase epitaxial growth catalysis by the presence of germane," <u>Applied Physics Letter</u> Vol. 56, No. 13 (March 26, 1990) pp. 1275-1277.							
	C28	Godbey et al., (1990) "Fabrication of Bond and Etch-Back Silicon Insulator Using a Strained SI <sub>0.7</sub> GE <sub>0.3</sub> Layer as an Etch Stop," <u>Journal of the Electrical Society</u> , Vol. 137, No. 10 (October 1990) pp. 3219-3223.							
	C29	Gray and Meyer, "Phase-Locked Loops"	', <u>A</u> n	alysis and Design of Ana	log Integrated Circuits (1984) pp. 605-632.				
· ·	C30	Grützmacher et al., "Ge segregation in SiGe/Si heterostructures and its dependence on deposition technique and growth atmosphere," <u>Applied Physics Letters</u> , Vol. 63, No. 18 (November 1, 1993) pp. 2531-2533.							
	C31	Hackbarth et al., "Alternatives to thick N (July 2000) pp. 148-151.	ИBE	-grown relaxed SiGe buff	ers," Thin Solid Films, Vol. 369, No. 1-2				
	C32	Hackbarth et al., "Strain relieved SiGe b Crystal Growth, Vol. 201/202 (1999) p			cture field-effect transistors," <u>Journal of</u>				
	C33	Herzog et al., "SiGe-based FETs: buffer	issu	sues and device results," Thin Solid Films, Vol. 380 (2000) pp. 36-41.					
	C34	<u>Thin Solid Films</u> , Vol. 336 (1998) pp. 141-144.							
	C35								
	C36	<ul> <li>C36 Höck et al., "High performance 0.25 μm p-type Ge/SiGe MODFETs," Electronics Letters, Vol. 34, No. 19 (September 17, 1998) pp. 1888-1889.</li> <li>C37 Huang et al., (2001) "Carrier Mobility enhancement in strained Si-on-insulatoir fabricated by wafer bonding", 2001 Symposium on VLSI Technology, Digest of Technical Papers, pages 57-58</li> </ul>							
	C37								
<del></del>	C38	Huang et al., "High-quality strain-relaxe <u>Physics Letters</u> , Vol. 76, No. 19 (May 8			ted silicon-on-insulator substrate," Applied				
	C39 Huang et al., "The Impact of Scaling Down to Deep Submicron on CMOS RF Circuits", IEEE Journal of Solid State Circuits, Vol. 33, No. 7, July, 1998, pp. 1023-1036.								
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EXAM. INIT.	ОТН	ER DOCUMENTS: (Including Author,	Title	Date, Relevant Pages,	Place of Publication)			
	C40	IBM Technical Disclosure Bulletin, Vol for Strain Relaxation of Si-Ge Layers o	0, "Optimal Growth Technique and Structure					
	C41 Ishikawa et al., "Creation of Si-Ge-based SIMOX structures by low energy oxygen implantation," Proceedings  IEEE International SOI Conference (October 1997) pp. 16-17.							
	C42	Ishikawa et al., "SiGe-on-insulator substance of (August 16, 1999) pp. 983-985.	trate	using SiGe alloy grown S	Si(001)," Applied Physics Letters, Vol. 75, No.			
	C43	C43 Ismail et al., "Modulation-doped n-type Si/SiGe with inverted interface," Appl. Phys. Lett., Vol. 65, No. 10 (September 5, 1994) pp. 1248-1250.						
	C44	Ismail, "Si/SiGe High-Speed Field-Effect 1995) pp. 20.1.1-20.1.4.	ct Tra	nsistors," <u>Electron Devid</u>	ces Meeting, Washington, D.C. (December 10,			
<u> </u>	C45	Kearney et al., "The effect of alloy scattering on the mobility of holes in a Si <sub>1-x</sub> Ge <sub>x</sub> quantum well," <u>Semicond. Sci</u> <u>Technol.</u> , Vol. 13 (1998) pp. 174-180.						
	C46	Kim et al., "A Fully Integrated 1.9-GHz CMOS Low-Noise Amplifier," <u>IEEE Microwave and Guided Wave Letters</u> , Vol. 8, No. 8 (August 1998) pp. 293-295.						
	C47	Koester et al., "Extremely High Transco Electron Device Letters, Vol. 21, No. 3	(Ma	rch 2000) pp. 110-112.				
	C48	1541-1547.			e Electronics, Vol. 41, No. 10 (1997), pp.			
	C49	König et al., "p-Type Ge-Channel MODFET's with High Transconductance Grown on Si Substrates," <u>IEEE</u> <u>Electron Device Letters</u> , Vol. 14, No. 4 (April 1993) pp. 205-207.						
	C50			Solid-State Electronics, Vol. 38, No. 9 (1995) pp. 1595-1602.				
	C51	(2002) pp. 288-295.		ed chemical vapor deposition," Materials Science and Engineering B89				
	C52	Kuznetsov et al., "Technology for high-performance n-channel SiGe modulation-doped field-effect transistors," <u>J Vac. Sci. Technol., B 13(6)</u> (November/December 1995) pp. 2892-2896.						
	C53	Langdo et al., (2002) "Preparation of Novel SiGe-free Strained Si on Insulator Substrates" <u>IEEE International SOI Conference</u> , pages 211-212 (XP002263057)						
	C54	Larson, "Integrated Circuit Technology Options for RFIC's - Present Status and Future Directions", <u>IEEE Journal of Solid-State Circuits</u> , Vol. 33, No. 3, March 1998, pp. 387-399.						
	C55	C55 Lee et al., "CMOS RF Integrated Circuits at 5 GHz and Beyond", Proceedings of the IEEE, Vol. 88, No. 10 (October 2000) pp. 1560-1571.						
	C56	C56 Lee et al., "Strained Ge channel p-type metal-oxide-semiconductor field-effect transistors grown on Si <sub>1-x</sub> Ge <sub>x</sub> /Si virtual substrates," <u>Applied Physics Letters</u> , Vol. 79, No. 20 (November 12, 2001) pp. 3344-3346.						
	C57	Lee et al., "Strained Ge channel p-type I Symp. Proc., Vol. 686 (2002) pp. A1.9			Ge <sub>x</sub> /Si virtual substrates," <u>Mat. Res. Soc.</u>			
EXAMI	NER			DATE CONSIDEREI	)			

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FORM	PTO –	1449	A'	TTY DOCKET NO.:	ASC-025DVC1			
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		OTHER ART. J	OU.	RNAL ARTICLES, I	ETC.			
EXAM. INIT.	ОТНЕ	ER DOCUMENTS: (Including Author, 7						
	C58	Leitz et al., "Channel Engineering of SiC Symp. Proc., Vol. 686 (2002) pp. A3.10			High Mobility MOSFETs," Mat. Res. Soc.			
	C59	Leitz et al., "Dislocation glide and block Physics, Vol. 90, No. 6 (September 15,			y graded SiGe/Si," <u>Journal of Applied</u>			
	C60 Leitz et al., "Hole mobility enhancements in strained Si/Si1-yGey p-type metal-oxide-semiconductor field-effect transistors grown on relaxed Si <sub>1-x</sub> Ge <sub>x</sub> (x <y) <u="" substrates,"="" virtual="">Applied Physics Letters, Vol. 79, No. 25 (December 17, 2001) pp. 4246-4248.</y)>							
	C61	Li et al., "Design of high speed Si/SiGe heterojunction complementary metal-oxide-semiconductor field effect transistors with reduced short-channel effects," <u>J. Vac. Sci. Technol.</u> , Vol. 20 No.3 (May/June 2002) pp. 1030-1033.						
	C62	Lu et al., "High Performance 0.1 μm Gate-Length P-Type SiGe MODFET's and MOS-MODFET's", <u>IEEE</u> <u>Transactions on Electron Devices</u> , Vol. 47, No. 8 (August 2000) pp. 1645-1652.						
	C63	Maiti et al., "Strained-Si heterostructure field effect transistors," <u>Semicond. Sci. Technol.</u> , Vol. 13 (1998) pp. 1225-1246.						
	C64	Maszara, "Silicon-On-Insulator by Wafer Bonding: A Review," <u>Journal of the Electrochemical Society</u> , No. 1 (January 1991) pp. 341-347.						
	C65	Meyerson et al., "Cooperative Growth Phenomena in Silicon/Germanium Low-Temperature Epitaxy," Applied Physics Letters, Vol. 53, No. 25 (December 19, 1988) pp. 2555-2557.						
	C66	Mizuno et al., "Advanced SOI-MOSFETs with Strained-Si Channel for High Speed CMOS-Electron/Hole Mobility Enhancement, " 2000 Symposium on VLSI Technology, Digest of Technical Papers, Honolulu, (June 13-15), IEEE New York, NY, pp. 210-211.						
	C67	C67 Mizuno et al., "Electron and Hold Mobility Enhancement in Strained-Si MOSFET's on SiGe-on-Insulator Substrates Fabricated by SIMOX Technology," <u>IEEE Electron Device Letters</u> , Vol. 21, No. 5 (May 2000) pp. 230 232.						
	C68	Mizuno et al., "High Performance Strained-Si p-MOSFETs on SiGe-on-Insulator Substrates Fabricated by SIMOX Technology," <u>IEEE IDEM Technical Digest</u> (1999) pp. 934-936.						
	C69	Nayak et al., "High-Mobility Strained-Si PMOSFET's"; <u>IEEE Transactions on Electron Devices</u> , Vol. 43, No. 10 (October 1996) pp. 1709-1716.						
	C70	O'Neill et al., "SiGe Virtual substrate N-channel heterojunction MOSFETS," Semicond. Sci. Technol., Vol. 14 (1999) pp. 784-789.						
	C71	Ota, Y. et al., "Application of heterojunction FET to power amplifier for cellular telephone," Electronics Letters, Vol. 30 No. 11 (May 26, 1994) pp. 906-907.						
	C72	Papananos, "Low Noise Amplifiers in MOS Technologies," and "Low Noise Tuned-LC Oscillator," Radio- Frequency Microelectronic Circuits for Telecommunication Applications (1999) pp. 115-117, 188-193.						
	C73 Parker et al., "SiGe heterostructure CMOS circuits and applications," Solid State Electronics, Vol. 43 (1999) pp. 1497-1506.							
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EXAM. INIT.	ОТНЕ	CR DOCUMENTS: (Including Author,	Title, l	Date, Relevant Pages,	Place of Publication)		
	C74	ım MOSFET's," <u>IEEE Transactions on</u>					
	C75	35, No. 6 (March 18, 1999) pp. 503-50	)4.		s on Si substrates," <u>Electronics Letters</u> , Vol.		
	C76	Rim et al., "Enhanced Hole Mobilities in	n Surfa	ace-channel Strained-Si	p-MOSFETs"; <u>IEDM</u> , 1995, pp. 517-520.		
	C77	Rim et al., "Fabrication and Analysis of Electron Devices, Vol. 47, No. 7 (July	Deep (2000)	Submicron Strained-Si pp. 1406-1415.	N-MOSFET's"; IEEE Transactions on		
<del></del> .	C78	Rim, "Application of Silicon-Based Het Effect Transistors", Ph.D. Thesis, Stanf	terostru ford U	ictures to Enhanced Moniversity (1999) pp. 1-1	bility Metal-Oxide-Semiconductor Field- 84.		
	C79 Robbins et al., "A model for heterogeneous growth of Si <sub>1-x</sub> Ge <sub>x</sub> films for hydrides," <u>Journal of Applied Physics</u> , 69, No. 6 (March 15, 1991) pp. 3729-3732.						
	C80 Sadek et al., "Design of Si/SiGe Heterojunction Complementary Metal-Oxide-Semiconductor Transistors," <u>IEEE Trans. Electron Devices</u> (August 1996) pp. 1224-1232.						
	C81 Sakaguchi et al., "ELTRAN® by Splitting Porous Si Layers," Proc. 195 <sup>th</sup> Int. SOI Symposium, Vol. 99-3, Electrochemical Society (1999) pp. 117-121.						
	C82	Schäffler, "High-Mobility Si and Ge Str 1549.	ructure	s," <u>Semiconductor Scie</u>	nce and Technology, Vol. 12 (1997) pp. 1515-		
·	C83	Sugimoto et al., "A 2V, 500 MHz and 3 IEICE Trans. Electron., Vol.E82-C, No.	3V, 920 o. 7 (Ju	MHz Low-Power Cur lly 1999) pp. 1327-132	rent-Mode 0.6 µm CMOS VCO Circuit", 9.		
	C84 Ternent et al., "Metal Gate Strained Silicon MOSFETs for Microwave Integrated Circuits", <u>IEEE</u> (October 200 pp. 38-43.						
	C85	Tsang et al., "Measurements of alloy co (June 15, 1994) pp. 8098-8108.	mposit	tion and strain in thin G	e <sub>x</sub> Si <sub>1-x</sub> layers," <u>J. Appl. Phys.</u> , Vol. 75 No. 12		
	C86	Tweet et al., "Factors determining the co	ition of strained GeSi lavember 14, 1994) pp. 2	tion of strained GeSi layers grown with disilane and germane," ember 14, 1994) pp. 2579-2581.			
	C87 Usami et al., "Spectroscopic study of Si-based quantum wells with neighboring confinement structure," Semicon.  Sci. Technol. (1997) (abstract).						
	C88 Welser et al., "Electron Mobility Enhancement in Strained-Si N-Type Metal-Oxide-Semiconductor Field-Effect Transistors," IEEE Electron Device Letters, Vol. 15, No. 3 (March 1994) pp. 100-102.						
	C89 Welser et al., "Evidence of Real-Space Hot-Electron Transfer in High Mobility, Strained-Si Multilayer MOSFETs," <u>IEEE IDEM Technical Digest</u> (1993) pp. 545-548.						
	C90 Welser et al., "NMOS and PMOS Transistors Fabricated in Strained Silicon/Relaxed Silicon-Germanium Structures," IEEE IDEM Technical Digest (1992) pp. 1000-1002.						
	C91	Welser, "The Application of Strained S Semiconductor Field-Effect Transistor	Silicon/ rs," Ph.	Relaxed Silicon Germa D. Thesis, Stanford Un	nium Heterostructures to Metal-Oxide- iversity (1994) pp. 1-205.		
	C92	Wolf et al., "Silicon Processing for the	VLSI	Era," Vol. 1 Process Te	chnology (1986) pp. 384-386.		
EXAMI	NER			DATE CONSIDERED			

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	C93	Xie et al., "Semiconductor Surface Roug Review Letters, Vol. 73, No. 22 (Noven		and Magnitude of Bulk Strain," The Physical					
·-	C94 Xie et al., "Very high mobility two-dimensional hole gas in Si/ Ge <sub>x</sub> Si <sub>1-x</sub> /Ge structures grown by molecular beam epitaxy," Appl. Phys. Lett., Vol. 63, No. 16 (October 18, 1993) pp. 2263-2264.								
	C95	Xie, "SiGe Field effect transistors," Mate	erials Science and Engineering	g, Vol. 25 (1999) pp. 89-121.					
	C96 Yamagata et al., "Bonding, Splitting and Thinning by Porous Si in ELTRAN®; SOI-Epi Wafer <sup>TM</sup> ," Mat. Res. Soc Symp. Proc., Vol. 681E (2001) pp. 18.2.1-18.2.10.								
	C97	Yeo et al., "Nanoscale Ultra-Thin-Body Silicon-on-Insulator P-MOSFET with a SiGe/Si Heterostructure Channel," <u>IEEE Electron Device Letters</u> , Vol. 21, No. 4 (April 2000) pp. 161-163.							
	C98 Zhang et al., "Demonstration of a GaAs-Based Compliant Substrate Using Wafer Bonding and Substrate Removal Techniques," Electronic Materials and Processing Research Laboratory, Department of Electrical Engineering, University Park, PA 16802 (1998) pp. 25-28.								
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